

Electronic Acknowledgement Receipt

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Title of Invention:	IDENTIFYING PROCESS AND TEMPERATURE OF SILICON CHIPS
First Named Inventor/Applicant Name:	Jagrut Viliskumar Patel
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1	Request for Continued Examination (RCE)	030439-RCE-5-13-09.pdf	36254 1a16da88f4101c1d7e1e1d41d513bbba0c a81e9	no	3
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3	NPL Documents	030439WO_2005-04-06_ISR.pdf	244570 3768731ff4d286c1a2afab0a14e8b1e080 9c99	no	2
Warnings:					
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4	NPL Documents	030439WO_2007-03-06_IPRP. pdf	777180 bb35c306d98a08027e042e7e5a5a60770f 841c8	no	5
Warnings:					
Information:					
5	NPL Documents	030439WO_2006-05-24_WO. pdf	703228 d6e1a5ee077f1ce80745c8013ed2515af0 08c2	no	5
Warnings:					
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6	Fee Worksheet (PTO-875)	fee-info.pdf	32173 d21e802ed0fe75db7a025d09fbaa8b1a47951 360a2	no	2
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